TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSV)

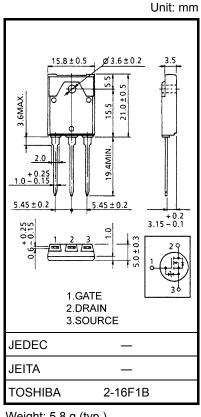
2SK2953

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance $RDS(ON) = 0.31 \Omega$ (typ.)
- High forward transfer admittance $|Y_{fs}| = 15 \text{ S (typ.)}$
- Low leakage current $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 600 \ V)$
- Enhancement mode : $V_{th} = 2.0 \sim 4.0 V (V_{DS} = 10 V, I_D = 1 mA)$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	600	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V _{DGR}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	۱ _D	15	А	
	Pulse (Note 1)	I _{DP}	60	A	
Drain power dissipation	n (Tc = 25°C)	PD	90	W	
Single pulse avalanche energy (Note 2)		E _{AS}	1026	mJ	
Avalanche current		I _{AR}	15	А	
Repetitive avalanche e	nergy (Note 3)	E _{AR}	9	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 5.8 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Thermal Characteristics

Characteristics	Symbol	Мах	Unit
Thermal resistance, channel to case	R _{th (ch−c)}	1.39	°C / W
Thermal resistance, channel to ambient	R _{th (ch−a)}	41.6	°C / W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 7.98 mH, R_G = 25 Ω , I_{AR} = 15 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

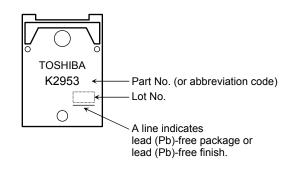
Electrical Characteristics (Ta = 25°C)

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V_{GS} = ±25 V, V_{DS} = 0 V	_		±10	μA
Gate-source bro	eakdown voltage	V (BR) GSS	I _G = ±10 μA, V _{DS} = 0 V	±30		_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V		_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold v	voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 10 V, I _D = 8.0 A		0.31	0.4	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 8.0 A	8.0	15.0	_	S
Input capacitance	xe	C _{iss}			3520	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz		20	_	pF
Output capacitance		Coss			300	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{_{0V}} \prod_{U} \stackrel{I_D=8.0A}{_{0V}} V_{OUT}$	_	50	_	
	Turn-on time	t _{on}		_	100	_	20
	Fall time	t _f		_	60	_	- ns
	Turn-off time	t _{off}	Duty $\leq 1\%$, t _w =10µs		325		
Total gate charge (gate-source plus gate-drain)		Qg		_	80	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 15 A		48	_	nC
Gate-drain ("miller") Charge		Q _{gd}			32	_	

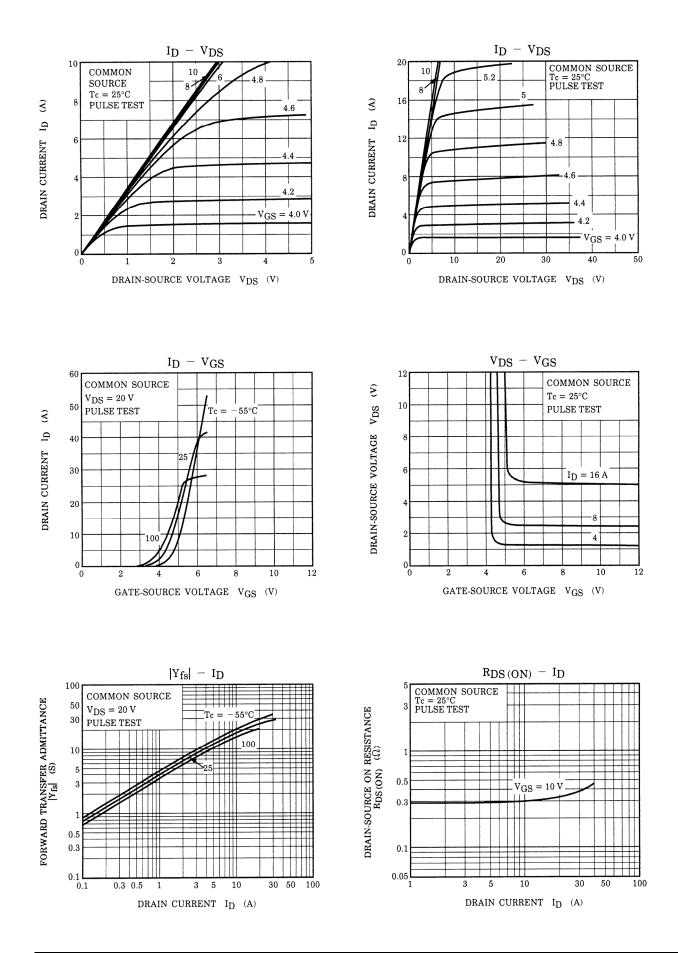
Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	15	А
Pulse drain reverse current (Note 1)	I _{DRP}	_			60	A
Forward voltage (diode)	V _{DSF}	I _{DR} = 15 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 15 A, V _{GS} = 0 V	_	620	_	ns
Reverse recovery charge	Qrr	dI _{DR} / dt = 100 A / µs		7.5		μC

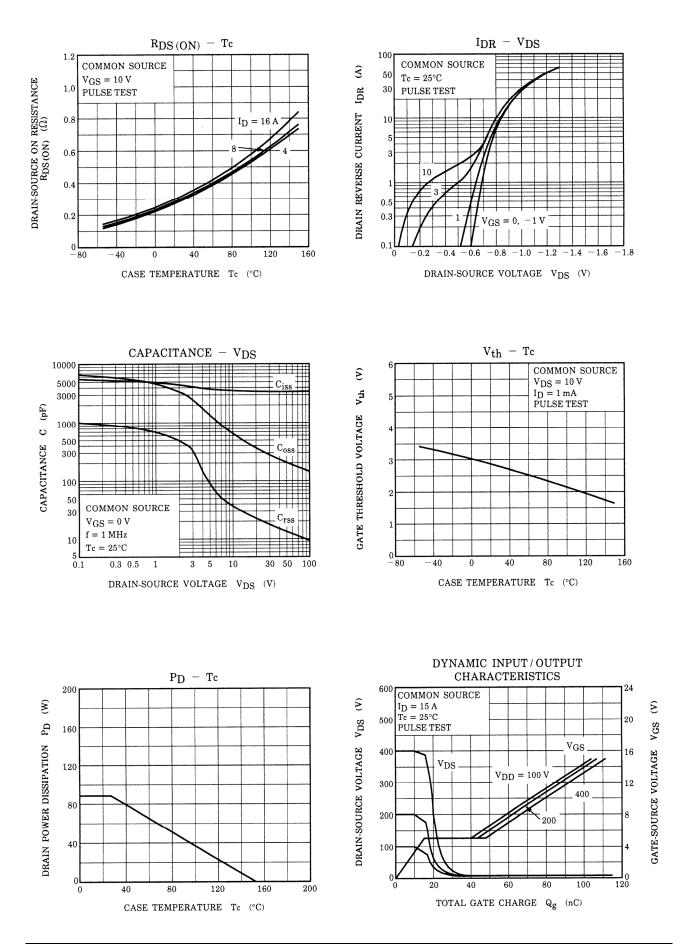
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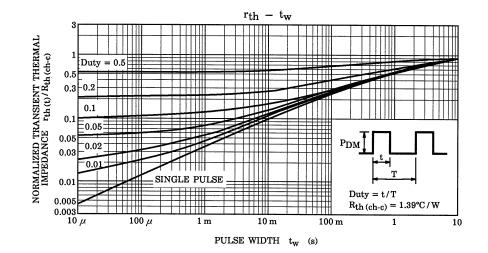
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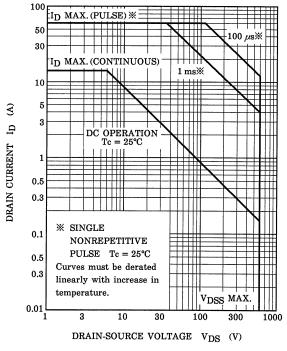
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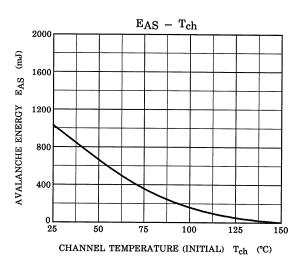


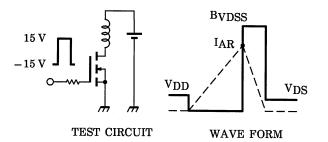
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SAFE OPERATING AREA







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